

Contributions to the newsletter should be sent to:

Ken Demers  
MS-48  
United Technologies Research Center  
East Hartford, Connecticut 06108  
203 727-7241

Other communications can be sent to:

John T. Rasted  
JTR Associates  
58 Rasted Lane  
Meriden, Conn. 06450  
203 634-1632

or

RT-11 SIG  
C/O DECUS  
One Iron Way  
MR2-3/E55  
Marlboro, Mass. 01752  
617 481-9511 Ext. 4141

#### FROM THE EDITOR

The article on TSX (multi-user RT-11) has generated much interest. However, I do not have Nigel Bevan's address in England. As soon as I receive it, I will publish it in the next "Minitasker". Those desiring his address earlier can call me. \*

To obtain a copy of the latest Symposium tape, if you live outside the U.S.A., contact:

Howard Schultens  
Physiologisches Inst. 2  
Humboldtallee 7  
D-3400 Goettingen  
W. Germany  
Telex: 96703  
Tel: 0551/395914

Ir. J. Loeve  
Head Dept. CRW/ASV  
Eramus University, Rotterdam  
P.O. Box 1738  
3000 Dr.  
Rotterdam, Holland

Ian Calhaem  
D.S.I.R  
P.O. Box 1320  
Wellington  
New Zealand

\*NOTE: I obtained Nigel's address just before printing:

Nigel Bevan  
National Physical Laboratory  
DNACS  
Teddington, Middx, TW11 0LW  
ENGLAND

#### RT-11 STEERING COMMITTEE

JOHN T. RASTED  
JTR ASSOCIATES  
58 RASTED LANE  
MERIDEN, CT 06450  
(203) 634-1632

RT-11 SIG CHAIRMAN

KEN DEMERS  
MS-48  
UNITED TECHNOLOGIES RESEARCH CENTER  
SILVER LANE  
EAST HARTFORD, CT 06108  
(203) 727-7241

NEWSLETTER EDITOR  
DECNET CONTACT

MARK BARTELT  
CALIFORNIA INST. OF TECH.  
MS 356-48  
1201 CALIFORNIA  
PASADENA, CA 91125  
(213) 796-6811 EXT 2883

RT-11 LUG COORDINATOR  
RT/RSX CONTACT  
WEST COAST

NICK BOURGEOIS / 1736  
SANDIA LABORATORIES  
P.O. BOX 5800  
ALBUQUERQUE, NM 87185  
(505) 844-8088

TAPE COPY COORDINATOR  
MACRO CONTACT

JAMES R. CUTLER  
SPACE PHYSICS RESEARCH LAB  
2455 HAYWARD  
ANN ARBOR, MICH 48105  
(313) 763-9940

STANDARDS COORDINATOR  
RT/RSX CONTACT  
MIDWEST

ART HERMES  
MIT/LNS BATES LINEAR ACCELERATOR  
P. O. BOX 95  
MIDDLETON, MA 01949  
(617) 245-6600

RT/RSX CONTACT  
EAST COAST

CARL LOWENSTEIN  
UNIVERSITY OF CALIF, SAN DIEGO  
MARINE PHYSICAL LABORATORY OF THE  
SCRIPPS INSTITUTION OF OCEANOGRAPHY  
SAN DIEGO, CA 92152  
(714) 294-3678

RT-11 HHK COORDINATOR  
SIG HARDWARE CONTACT

ERIC MORTON  
SANDERS TECH. INC.  
P.O. BOX 1226  
NASHUA, NH 03061  
(603) 882-1000 EXT 253

JOHN L. PRATHER  
PA COLL. OF POD. MED.  
8TH LACE STREETS  
PHILADELPHIA, PA 19107  
(215) 629-0300 EXT 155

SUSAN S. RASTED  
SOFTWARE DYNAMICS INC.  
1000 YALE AVE.  
WALLINGFORD, CT 06492  
(203) 265-2226

MARILYN RUNYON  
39 LOCUST POINT RD.  
LOCUST, N.J. 07760

EDWIN J. STEVENS  
EMDA INC.  
899 EAST GREEN ST.  
PASADENA, CA 91106  
(213) 795-5991

RAY STRACKBEIN  
CHALFRONT COMMUNICATIONS  
73-680 HIGHWAY 111  
PALM DESERT, CA 92260  
(714) 346-3987

J. W. TIPPIE  
ARGONNE NATIONAL LABS  
9700 S. CASS AVE  
ARGONNE, ILL 60439

GREG WOODBURY  
DEPT. OF MICROBIOLOGY  
BOX 3020  
DUKE UNIV. MED. CENTER  
DURHAM, N.C. 27710  
(919) 684-5138

RT-11 DECUS LIBRARY  
COORDINATOR  
C CONTACT

FORTTRAN CONTACT

FMS-11 CONTACT

WISH LIST

MU-BASIC CONTACT

SYMPOSIA COORDINATOR

CAMAC CONTACT

BASIC CONTACT

#### DECUS RT-11 & GENERAL LOCAL USER GROUPS

##### ALAMO PDP-8 & PDP-11 LOCAL USERS GROUP

Chairman: Douglas Threatt  
USAF School of Aerospace Medicine  
Biometric Division (BRP)  
Brooks AFB, TX 78235  
(512) 536-3886

##### ALBUQUERQUE RT-11 LOCAL USERS GROUP (ARTLUG)

Chairman: N.A. Bourgeois, Jr.  
Sandia Labs 11736  
P.O. Box 5800  
Albuquerque, NM 87185  
(505) 264-8088

##### BAY AREA PDP-11 LOCAL USERS GROUP (BUG)

Chairman: Joan Salkin  
806 Taurus Drive  
Foster City, CA 94404  
(415) 349-3173

##### BAY RT-11 LOCAL USERS GROUP

Chairman: Lyle Ryan  
Electronic Engineer  
CETUS  
600 Bancroft Way  
Berkeley, CA 94710  
(415) 549-3300

##### BIOMEDICAL COMPUTING UCLA LOCAL USERS GROUP

Chairman: Dr. R.M. Harper  
UCLA  
Dept. of Anatomy  
Los Angeles, CA 90024

##### CAROLINA'S LOCAL USERS GROUP

Chairman: John Scogin, III  
Lockwood Greene Engineers  
P.O. Box 491  
Spartanburg, SC 29304  
(803) 582-2132

##### CENTRAL ALABAMA PDP-11 LOCAL USERS GROUP

Chairman: Roy Swatzell  
V.A. Hospital  
700 S. 19th St.  
Birmingham, AL 35233  
(205) 933-8101 X239

CENTRAL OKLAHOMA PDP-ELEVEN USERS SOCIETY (COPEUS)

Chairman: Mike O'Dell  
University of Oklahoma  
202 W. Boyd  
Room 107  
Norman, OK 73019  
(405) 325-2621

CHESAPEAKE AREA RT-11 LOCAL USERS GROUP

Chairman: Henry Dardy  
Naval Research Laboratory  
Code 8133  
Washington, D.C. 20375  
(202) 767-3336

CHINA LAKE RT-11 LOCAL USERS GROUP

Chairman: Michael Levine  
Naval Weapons Center  
Code 3513  
China Lake, CA 93555  
(714) 939-3575

EASTERN TENNESSEE PDP-11 LOCAL USERS GROUP

Chairman: Kenneth J. Cross  
Oak Ridge National Lab  
Box X  
Bldg. 3500  
Oak Ridge, TN 37830  
(615) 574-5729

FLORIDA PDP-11 LOCAL USERS GROUP

Chairman: Robert Carson  
Rollins College  
Physics Dept.  
Winter Park, Fla. 32789  
(305) 646-2000 X2517

FLORIDA USERS OF RT-11 (FLURT)

Chairman: Patrick Perrott  
PELCOR  
1607 Forsyth Rd.  
Orlando, FL 32801  
(301) 275-1132

ITHACA MINICOMPUTER LOCAL USERS GROUP (IMLUG)

Chairman: Ray Helmke  
Cornell University  
Wilson Laboratory  
Ithaca, NY 14853  
(607) 256-4882

MADISON WISCONSIN LOCAL USERS GROUP

Chairman: Richard Marleau  
University of Wisconsin  
Dept. of Elec. & Comp.  
Madison, WI 53706  
(608) 262-1191

METRO ED LOCAL USERS GROUP

Chairman: Bruce Alcock  
Riverdale Country School  
West 253rd St. & Fieldstone Rd.  
Bronx, NY 10471  
(212) 549-8810

MIDLANDS LOCAL USERS GROUP

Chairman: Donal Caviness  
Air Force Global Weather Central  
Offutt AFB, NE 68113  
(402) 294-3073

MINNESOTA LOCAL USERS GROUP (MINNELUG)

Chairman: Richard Newman  
University of Minnesota  
Biomedical Center  
Diehl Hall  
Minneapolis, MN 55455  
(612) 373-2156

MIT RT-11 LOCAL USERS GROUP (MITLUG)

Chairman: Thomas Provost  
MIT/LNS Bates Linear Accelerator  
P.O. Box 95  
Middleton, MA 01949  
(617) 245-6600

NEW ENGLAND USERS OF RT-11 (NEURT)

Chairman: Eric Morton  
Sanders Tech Inc.  
P.O. Box 1226  
Nashua, NH 03061  
(603) 882-1000 X253

NORTHWEST LSI EDUCATIONAL LOCAL USERS GROUP

Chairman: Ron Tenison  
Catlin Gabel School  
8825 SW Barnes Road  
Portland, OR 97225  
(503) 297-1894

NY METRO LOCAL USERS GROUP

Chairman: John Runyon  
FTCC  
25 Broad St.  
New York, N.Y. 10004  
(212) 747-5573

PDP-11 SEABOARD LOCAL USERS GROUP

Chairman: Karen Hill  
Princeton Plasma Physics  
C-Site  
Forrestal Campus  
Princeton, N.J. 08540  
(609) 452-5092

PENN STATE LOCAL USERS GROUP

Chairman: Rich Hoover  
Penn State University  
USDA Pasture Res. Lab.  
University Park, PA 16802

PHOENIX LOCAL USERS GROUP

Chairman: Ken Bates  
Realtime Systems  
4621 N. 16th St.  
Suite E-509  
Phoenix, AZ 85016

PITTSBURGH AREA LOCAL USERS GROUP

Chairman: Richard Pethia  
LRDC Bldg.  
University of Pittsburgh  
3939 O'Hara St.  
Pittsburgh, PA 15260  
(412) 624-4905

PORTLAND AREA PDP-11 LOCAL USERS GROUP

Chairman: Bill Bagley  
Stan Wiley Realtors  
200 SW Market  
Suite 1800  
Portland, OR 97201  
(503) 222-5561 X7472

SAN DIEGO PDP-11 LOCAL USERS GROUP

Chairman: Rich Lavery  
NARF Bldg. 463, Code 332  
North Island  
San Diego, CA 92135

SOUTHEASTERN MICHIGAN PDP-11 LOCAL USERS GROUP

Chairman: James Cutler  
University of Michigan  
Space Physics Res.  
2455 Hayward  
Ann Arbor, MI 48105  
(313) 764-6587

SOUTHERN CALIFORNIA USERS OF RT-11 (SCURT)

Chairman: Ray Strackbein  
Chalfont Communications  
73-680 Highway 111  
Palm Desert, CA 92260  
(714) 346-3987

SPOKANE AREA LOCAL USERS GROUP

Chairman: Jerry Clark  
American Sign & Indicator  
P.O. Drawer 2727  
Spokane, WA 99220  
(509) 535-4101

ST. LOUIS PDP-11 LOCAL USERS GROUP

Chairman: Richard Aurbach  
Monsanto Agricultural  
Products Company  
800 N. Lindbergh  
St. Louis, MO 63166  
(314) 694-5453

UPPER VALLEY PDP-11 LOCAL USERS GROUP

Chairman: Steve Ladd  
CREARE Inc.  
P.O. Box 71  
Hanover, NH 03755  
(603) 643-3800

WESTERN RESERVE USERS GROUP (WRUG)

Chairman: John Kremer  
U.S. Steel-Lorain Works  
1807 E. 28th St.  
MS 10B  
Lorain, OH 44055  
(216) 277-2890 or 2403

---

## USER REQUESTS

---

If anyone has any information on a handler for the LA36 that will run under RT-11 V3B ( with Multi-Terminal support ), it would be greatly appreciated. I did not include the DL-11 that the LA36 is attached to as one of the Multi-Terminal devices. It is free to be used as a handler controlled device. Please send any information to:

Ron Pipes  
U.S. Borax Research Corp.  
412 Crescent Way  
Anaheim, Calif. 92801

---

## USER RESPONSES

---

Reply to Randy Lee, Nov. '79 Mini-Tasker --

When using TECO, the quickest way to insert ASCII characters is the <n>I\$ command. This inserts the character whose ASCII value is <n> ( <n> is interpreted in the current radix ). To insert the right set bracket symbol, "]" , the command 125I\$ should be given if the current radix is 10, otherwise 175I\$ should be given for radix 8. The same can be done for the tilde, "~".

If the character will be used frequently ( like the set brackets in C or Pascal ) then the character could be stored in a Q-register. Then, when the character is needed, the character would just be retrieved.

This is not as easy as just typing the character, but it will work until RT-11 is changed to allow the characters at the high end of the ASCII set to be entered without converting them to escapes.

## ENHANCEMENTS TO VT52.TEC AND VEG.TEC

In the RT-11 V3B distribution kit there are some files that contain TECO macros; two of these are to give VT52 users a powerful screen editor with cursor control. The first macro, VEG.TEC, is just a startup macro. It sets the name of the file to be edited and loads the screen control macro, VT52.TEC, and the file. The macro will also give help if the user requests it ( the available commands are shown ). The VT52 macro is the controller for the screen editor. With VT52 running, the entire screen becomes a window into the file, with the cursor showing where the pointer is. Commands are entered through the numeric keypad or as control characters ( the help part of VEG.TEC tells what all the keys mean ). Any characters that are not commands are immediately inserted, and the screen is updated. The editor can be learned in a few minutes, and all editing is much faster and easier than with standard TECO or EDIT.

The first problem with the screen macros is that they do not work under the FB monitor until a SET TT NOCRLF is given. Without it, the screen fills up with garbage. I have a command file to start up the editor called SCOPE.COM as follows:

```
SET TT NOFB
SET TT NOPAGE
SET TT NOCRLF
EDIT/EXECUTE/TECO SY:VEG.TEC
SET TT CRLF
SET TT PAGE
SET TT FB
```

Now for editing, I just enter @SCOPE and everything is taken care of.

After using the editor for awhile, I thought of some nice enhancements that would suit my needs a little better. The following changes are the results:

To VEG.TEC --

1. The file size can be entered using the square brackets ( [ and ] ).
2. The command summary and keyboard map is printed only if a question mark is entered for the file name. After printing the help, it asks for the filename again.
3. The old file name ( which is saved from one edit to the next so that if you are editing a file several times, you don't need to reenter the file name ) can be edited. If you want to change just one character instead of erasing the whole name and retyping the new one, backspace the cursor to where you want the change, delete the old characters and type the new ones.

4. If the file EDTMAC.TEC is on SY:, it is read in and placed in the Q-register G as a macro. This allows a macro that is large to be saved in a file. It is handy if a certain editing function is needed regularly enough to warrant writing a TECO macro.

5. File name echo is much quicker.

To VT52.TEC --

New Control character commands -

- ctrl-G - Executes the macro that VEG.TEC read from EDTMAC.TEC
- ctrl-P - Equivalent to <esc>24. Used as a Page command. Example: ctrl-P followed by cursor down will move the cursor 24 lines down, thus bringing a completely new page onto the screen.
- ctrl-A - Add lines to the R buffer. This puts lines into the secondary save buffer (besides the one controlled by the numeric pad).
- ctrl-E - The enters characters into the R buffer. The difference between ctrl-A and ctrl-E is that <esc><n> ctrl-A puts <n> LINES into the buffer, while <esc><n> ctrl-E puts <n> CHARACTERS into the buffer. This is handy for saving only parts of lines.
- ctrl-R - This retrieves whatever was in the R buffer and inserts where the cursor is.
- ctrl-Z - Does an EX command. If <esc> ctrl-Z is entered, the file is closed, and a @EDTFIN.COM is given. This file can contain a command to compile the last file edited (COMPILE @SY:EDIT.FIL) or any other monitor commands.

For those who have not used VT52.TEC, these are only the commands I added. There are already commands for cursor movement, string searching, inserting text, deleting text (lines or characters), moving blocks of text, plus the ability to enter direct TECO commands. All in all, the screen editor is a big improvement over the standard hardcopy terminal type of editor.

If you have TECO and would like a copy of the macros for screen editing (the improved ones), send a floppy disk to:

Ron Pipes  
U.S. Borax Research Corp.  
412 Crescent Way  
Anaheim, Calif. 92801

11.

# USER INPUT

DATBAS (DECUS 11-294) -- ERROR OF OMISSION

This refers to a set of BASIC-11 V2 extension routines that permit reading and writing at any implemented bus address. A user has reported a problem with the GETB routine. When the variable type is numeric (AZ or A) the value of the returned argument ranges from 0 to 255 rather than from -128 to 127. This problem may be avoided by rescaling the value after its return to BASIC as follows:

```
CALL GETB(A$,B)
IF B=>128 THEN B=B-256
```

When I get around to fixing this, I'll resubmit it to the DECUS library.

Nick Bourgeois



Dear Mr. Demers:

Most of our 8080/8085 software is developed on our PDP-11 (RT-11 V03). A cross assembler creates an Intel formatted ASCII hex file on an RX01 floppy disk. Some months ago we purchased a Data I/O system 19 PROM programmer, and we needed a program to transmit the hex file from the disk to the Data I/O's RAM.

Enclosed is a listing of the program that has worked well for us. Other DECUS members may find it useful.

We chose to use the current loop serial link, and this choice necessitated hardware changes. The opto-isolator on the Data I/O was changed to a faster one. Also, the Data I/O supplies only 2mA to the loop that detects transmissions from the PDP-11. Therefore, we had to install a booster circuit to present the proper current to the PDP 11's DLV-11. Diagrams showing the changes are enclosed.

Very truly yours,

*Ted Knowlton*  
Ted Knowlton  
Software Engineer

12.



```

1      LIST TIM      ;NARROW LISTING
2      ENABL LC      ;ENABLE LOWER CASE
3
4      ; TRANSMIT INTEL HEX FORMAT FILE FROM PDP 11 DISK
5      ; TO DATA I/O SYSTEM 19 PROGRAMMER
6
7      ;
8      ;          REVISED   27 DEC 79
9
10     ; This program transmits a 4K (max.) portion of
11     ; an Intel format Hex file from PDP 11 (RT-11 V03)
12     ; to a DATA I/O System 19 Prom Programmer.
13     ; Before calling this program, put desired PROM
14     ; module in Programmer. Turn on power, press
15     ; "SELECT", then "FL" and "START". Programmer is
16     ; then under remote control from RT-11.
17     ; After "F", type one Hex file name. After file is
18     ; read and prompt displayed, type starting Hex
19     ; address - which must be at 1K boundaries;
20     ; e.g. 0, 400, 800, C00, 1000, 1400 etc.
21     ; After typing "Return", go to Programmer, wait
22     ; for completed transmission, exit remote control,
23     ; (empty data), program PROM's, reenter remote
24     ; control; continue with another 4K portion, if
25     ; desired.
26
27     ; NOTE: The buffer size set for this program is
28     ; 40K bytes. At 43 characters/record and 16 data
29     ; bytes/record, the buffer will hold what amounts
30     ; to about 15K bytes of object code. A hex file
31     ; that is too big may be split up. It will be
32     ; necessary to edit in a ".00" at the split so
33     ; that program will recognize end of file.
34
35     .MCALL .CSIGEN, .READW
36     .MCALL .GTIN, .TTYOUT
37     .MCALL .EXIT, .SETUP
38     .MCALL .CLOSE
39
40     ; DLV11 ADDRESSES
41
42     175610 RCSR=175610      ; RCSR CSR
43     175612 RCRB=RCR+2      ; RCV BUFFER
44     175614 XCSR=RCR+4      ; XMIT CSR
45     175616 XCBP=RCR+6      ; XMIT BUFFER
46
47     ; MASKS FOR XCSR, RCSR
48     000200 RCVBN=200      ; RCVG BINS
49     000200 XMRDY=200      ; XMIT READY
50
51     ; For debugging use:
52     ; LINK/MAP: TT:/DEBUG FROM DBU
53
54     000000 FROM: ; DOUBLE COLON FOR GLOBAL SYMBOL (FOR DBU)
55
56     000000 013704 .SETUP #HEXBEE      ; SET HIGHEST ADRS
57     000012 013704 MOV @#RBUF, R4      ; CLEAR DLV11
58     000016 013704 MOV #MSG1, R4
59     000022 012704 XN: .TTYOUT (R4)+    ; TYPE MSG ON DECSCOPE
60     000030 020427 CMP R4, #MSG1
61     000034 002604 BNE XN
62     000036 012700 XMRST:
63     000042 000002 MOV #<MSG2-MSG2>, R0
64     000046 012704 MOV #MSG2, R4
65     000046 004567 JSR R5, XMTMSG      ; XMIT "H" (RET)
66     000052 001050 TST R3
67     000054 001011 BNE XN1      ; BR IF NO PROM

```

```

68 000056 012704 XME:
69 000056 002606 MOV #MSG3, R4      ; COMMUNICATION ERROR
70 000062 012704 XME1: .TTYOUT (R4)+
71 000070 020427 CMP R4, #MSG3
72 000074 002640 BNE XME1
73 000076 001372 .EXIT
74
75 000100 012700 XN1:
76 000100 000004 MOV #<MSG4-MSG4>, R0
77 000104 012704 MOV #MSG4, R4 ; 83A (Intel format)
78 000110 002640 JSR R5, XMTMSG
79 000114 005703 TST R3
80 000116 001757 BEQ XME
81
82 000120 012700 MOV #<MSG6-MSG6>, R0
83 000124 000002
84 000130 012704 MOV #MSG6, R4      ; U (add LF)
85 000134 004567 JSR R5, XMTMSG
86 000136 000766
87 000140 005703 TST R3
88 000140 001747 BEQ XME
89 000154 005005 .CSIGEN @#50, #DEFTY, #0 ; GET FILE NAME, ETC
90 000156 CLR R5
91 000214 .READW #ARGBL, #3, #HEXBFL, #HEXBFL, R5
92 000222 .CLOSE #3
93 000222 BR START
94
95 000224 RESTRT:
96 000224 MOV #MSG8, R4
97 000230
98 000242 020427 RST1: .TTYOUT (R4)+
99 000242 002704 CMP R4, #MSG8
100 000244 001372 BNE R511
101
102 000244 012702 START:
103 000270 001366 GTIN #LINBUF, #PROMPT      ; GET STARTING ADRS
104 000272 005001 MOV #LINBUF, R2
105 000272 CLR R1      ; INITIALIZE R1
106 000274 012200 STABL:
107 000274 001425 MOV (R2)+, R0      ; END OF PARAM INPUT
108 000302 002350 BEQ RESTRT      ; TOO MANY CHARS
109 000304 004567 JSR R5, HEXTEST      ; CHECK FOR ASCII HEX CHAR
110 000310 005703 TST R0
111 000312 001744 BEQ RESTRT      ; START OVER IF NOT LEGIT CHAR
112 000314 022700 CMPB #0, R0
113 000320 001410 BEQ START5      ; CHECK POS'N OF ZERO
114 000322 005701 TST R1
115 000324 001404 BEQ START3      ; BR IF NOTHING ENTERED
116 000326 032701 START2:
117 000326 001372 BIT #177400, R1
118 000332 001357 BNE START1      ; BR IF 2 CHARS ALREADY ENTERED
119 000334 000301 SWAB R1
120 000336 060001 START3:
121 000336 060001 ADD R0, R1
122 000340 000754 BR START1      ; GLT ANOTHER CHAR
123
124 000342 005701 .ASCII /ERO RCVB
125 000342 START5:
126 000342 TST R1

```



```

127 000344 001752 BEO START1 ;BR IF LEADING ZERO
128 000346 000767 BR START2
129
130 000350 005701 PARAM:
131 000352 001005 TST R1
132 000354 012701 BNE PAR1 ;BR IF NOT 0
133 000356 030460 MOV #30460, R1 ;ASCII <00> (=HI LBR1)
134 000360 012702 MOV #30460, R2 ;ASCII <10> (=HI LBR1)
135 000364 030464 BR OUTPAR
136 000366 000454 PAR1:
137 000368 020227 ; 2 ASCII HEX CHARS IN R1
138 000370 001372 CMP R2, #LINBUF+4
139 000372 001027 BNE PARE ;BR IF NOT 3 CHARS TOTAL RCVD
140
141 000374 122701 ; 3 CHARS TOTAL RCVD. MUST=04,08,0C ELSE->START
142 000376 000060 CMPB #0, R1
143 000400 001311 BNE RESTRT ;ILLEGAL IF NOT <0>
144 000402 000301 SWAB R1
145 000404 010102 MOV R1, R2
146 000406 122702 ; PREPARE R2=2K HIGHER THAN R1
147 000408 000064 CMPB #4, R2
148 000412 001003 BNE PAR01
149 000414 012702 MOV #30464, R2 ;LOW=400H, HI=1400H
150 000420 000436 BR OUTPAR
151 000422 122702 PAR01:
152 000424 000070 CMPB #8, R2
153 000426 001003 BNE PAR03
154 000430 012702 MOV #30470, R2 ;LO=800H, HI=1800H
155 000434 030470 BR OUTPAR
156 000436 000430 PAR03:
157 000438 122702 CMPB #C, R2
158 000442 000103 BNE RESTRT
159 000444 012702 MOV #30503, R2 ;LO=000H, HI=1000H
160 000450 000422 BR OUTPAR
161
162 000452 122701 ; 4 CHARS RCVD -- EVEN
163 000454 001267 PARE:
164 000456 020227 CMP R2, #LINBUF+5
165 000458 001267 BNE RESTRT ;MIN 4 CHARS ELSE ERROR
166 000460 122701 CMPB #0, R1
167 000464 000060 BEO PARE1
168 000466 001411 CMPB #4, R1
169 000472 000064 BEO PARE1
170 000474 001406 CMPB #8, R1
171 000500 000070 BEO PARE1
172 000502 001403 CMPB #C, R1
173 000506 001244 BNE RESTRT
174 000510 010102 PAR01:
175 000512 062702 MOV R1, R2
176 000514 000400 ADD #400, R2
177
178 ; NOTE: NO HIGH ADDRESS LIMIT IS TESTED

```

```

1 000516 OUTPAR:
2
3 ; R1=HIGH ORDER 2 ASCII HEX CHARS SPECIFYING
4 ; LOW LIMIT, E.G. 04 => 400
5 ; 2C => 2C00
6 ; R2=AS IN R1 SPECIFYING HIGH LIMIT,
7 ; E.G. 14 => 1400=400+1000
8 ; 3C => 3C00=2C00+1000
9
10 000518 010100 MOV R1, R0
11 000520 004567 JSR R5, XMTNR ;XMIT VIRTUAL MEMORY OFFSET
12 000524 000532 MOV #MSG7-MSG7>, R0
13 000530 000004 MOV #MSG7, R4 ;LOW CR?
14 000534 012704 JSR R5, XMTMSG ;REST OF VIR MEM CMD
15 000540 000303 TST R3
16 000542 001002 BNE OP1 ;OK
17 000544 000187 JNP XME
18 000550 177306 OP1:
19 000552 012700 MOV #44415, R0 ;<1> CR
20 000554 004567 JSR R5, XMTNR
21 000560 005003 CLR R3
22 000562 005303 OP2:
23 000564 001376 DFC R3
24 000566 012704 BNE OP2 ;INFLAY
25
26 000568 002706 MOV #HEXBUF, R4
27 000572 005067 CLR NTLF
28 000574 005067 CLR NTLCA
29 000534
30 000602 ;SEARCH PARAMETERS
31 000604 SPAR:
32 000606 TST NTLF
33 000610 BEO SP1AA
34 000612 MOV LCA, R4 ;LAST COLUMN ADDR
35 000614 000520 SP1AA:
36 000616 122427 CMPB (R4)+, #0
37 000618 000072 BNE SPAR
38 000620 001370 MOV R4, R3
39 000622 010405 DFC R3 ;R5=ADRS WHERE <1> FOUND
40 000624 005305 CMPB (R4)+, #0
41 000626 122427 BEO SP1A
42 000628 000403 INC R4
43 000630 BR SP1B
44 000632 122427 SP1A:
45 000634 000060 CMPB (R4)+, #0
46 000636 001466 BEO SP7 ;BR IF EOF. CHAR CNT=0
47 000638 112400 SP7:
48 000640 000300 MOVB (R4)+, R0
49 000642 000300 SWAB R0
50 000644 112403 MOVB (R4)+, R3
51 000646 060300 AND R3, R0 ;R0 NOW=HIGH 2 OF RECORD ADDR
52 000648 020001 CMP R0, R1 ;LOW LIMIT
53 000650 100474 RMT SP11 ;BR IF ADRS < LOW LIMIT
54 000652 001024 BNE SP1D
55 000654 005767 TST NLF
56 000656 000442 BEO SP1C ;BR IF NOT FLAGGED
57 000658 001403 CLR NTLF
58 000660 000434 BR SP1F
59 000662 000420

```

```

58 000700      SP10:
59      ; EXAMINE L (0, 2 ADRS BYTES (LOZ))
60      ; IF LOZ=0X, WHERE X < 0, TRANSMIT PREVIOUS
61      ; RECORD BECAUSE IT CONTAINS SOME DATA WITHIN
62      ; CHOSEN PARAMETERS. SET FLAG.
63 000700 122427 CMPB (R4)+, #<0
64 000704 001015 BNE SP1E
65 000706 122427 CMPB (R4)+, #<0
66 000712 000060 BEQ SP1F
67 000714 010567 ; CONDITIONS MET
68      MOV R5, LCA ;SAVE THIS COLON'S ADR
69 000720 000414 BIS #377, NTLF ;SET FLAG
70 000726 016705 MOV NTLCA, R5 ;ADRS OF PREVIOUS RECORD
71 000732 000402 RR SP1F
72 000734      SP1D:
73 000734 020007 CMP R0, R2
74 000736 002321 BGE SPAR ;BR IF ADRS >= HI LIMIT
75 000740      SP1E:
76 000740      ; RECORD DATA ADRS IS WITHIN CHOSEN PARAMS.
77      MOV R5, R4
78 000742 121427 SP1:
79 000742 000012 CMPB (R4), #12 ;LF
80 000742 001412 BEQ SP3
81 000746 000012 SP3:
82 000750 033727 BIT #XCSR, #XPRDY
83 000750 000200
84 000756 001774 BEQ SP3
85 000760 112437 MOVE (R4)+, @#XBUF ;XMIT CHAR
86 000764 005705 TST R5
87 000766 001705 BEQ SPAR ;BR IF END OF RECORD
88 000770 010405 MOV R4, R5 ;MAKE R5 < 0
89 000772 000763 BR SP1
90      ; END OF RECORD
91 000774      SP5:
92 000774 005005 CLR R5
93 000776 033727 BIT #RCSR, #RCVDN
94 001004 000200
95 001006 001761 BEQ SP3 ;XMT & CONTINUE SEARCH
96 001014 123727 CMPB @#RBUF, #12
97 001016 175612
98 001018 000012 BEQ SP3
99 001018 001755 JMP XME ;COMMUN. ERROR
100 001022      ; END OF FILE
101 001022 012700 SP7:
102 001026 000005 MOV #<MSG5-MSG5>, R0
103 001032 012704 MOV #MSG5, R4 ;:00
104 001036 002644 JSR R5, XMMSG
105 001040 004567
106 001042 000064 TST R3
107 001046 005703 BNE XA?
108 001048 000167 JMP XMF
109 001046 177010
110 001052      SP9:
111 001052 000167 JMP START ;GUT READY FOR ANOTHER INPUT
112 001052 177172
113 001052      SP11:
114 001052 010567 MOV R5, NTLCA ;SAVE ADRS OF THIS COLON
115 001052 000260

```

```

111 001056 000656 BR SPIAA
112
113
114      ; SUBROUTINE:
115      ; TEST CONTENTS OF R0 FOR ASCII HEX DIGIT.
116      ; CAUSE BRANCH (ON RETURN) TO "START" IF FAIL.
117
118 001060      HEXST:
119 001060 012703 MOV #1, R3
120 001064 000001
121 001070 120027 CMPB R0, #<0
122 001072 000060
123 001076 103412 BLD HEXTF
124 001100 120027 CMPB R0, #<F
125 001104 000071 BHI HEXTF
126 001106 003403 CMPB R0, #<9
127 001112 000071 BLE HEXTX
128 001114 103401 BLD HI XTF
129 001116 000205 HEXTX:
130 001116 000205 RTS R5
131 001116 005308
132 001120 000205 DEC R3
133
134      ; TRANSMIT MSG CODE TO DATA I/O PROGRAMMER;
135      ; WAIT RESPONSE
136      ; RC=CHAR COUNT OF MSG
137      ; R1,R2 NOT USED NOR ALTERED
138      ; R3=0 ON RETURN, ONLY IF ERROR
139      ; R4=ADRS OF MESSAGE
140      ; R0,R3 USED AS TIMER, INTERNALLY
141
142 001122      XMMSG:
143 001122 113703 MOV @#RBUF, R3 ;CLEAR & RESET
144 001126 175612 MOV @#RBUF, R3 ;RBUF & RCSR
145 001132 005003
146 001132 005003
147 001134 033727 XMT1A:
148 001134 175612 CLR R0
149 001142 000200
150 001144 001003 BIT @#XCSR, #XPRDY
151 001146 005303 BNE X1A ;BR IF READY
152 001150 001372 DEC R3
153 001152 000417 BNE XMT1
154 001152 112437 BR XEX
155 001156 175616 X1A:
156 001160 005300 MOV (R4)+, @#XBUF
157 001162 001364 DEC R0
158 001162 005000 BNE XMT1A
159 001164 012703 X1B:
160 001170 000001 CLR R0
161 001170 033727 MOV #1, R3
162 001176 175610
163 001176 000200 BIT @#RCSR, #RCVDN
164 001200 001006
165 001202 005300 BNE X3
166 001204 001372 DEC R0
167 001210 005303 BNE X1
168 001210 001370 BLD X3
169 001212 005003 XEX:
170 001214 000205 CLR R3 ;SIGNAL ERROR
171 001214 113700 RTS R5
172 001214 113700 MOV @#RBUF, R0

```

```

177 001220 175612      CHPB R0, #12      ;LF
          120027
          000012
178 001224 001756      BEQ X1B
179 001226 120027      CHPB R0, #12
          000076
175 001232 001366      BNE XEX      ;ERROR IF NOT
176 001234 003727      BIT @#RCSR, #RCVDN
177 001234 175610
          000200
178 001242 001774      BEQ X4      ;WAIT TIL CR IS RCVD
179 001244 113700      MOVB @#RBUF, R0      ;READ & RESET STATUS BIT
          175612
480 001250 012703      MOV #1, R3
          000001
181 001254 000205      RTS R5      ;MSG ACKNOWLEDGED OK
          182
          183
          184
185 001256 012704      XMTNR:      NO RESPONSE AWAITED
          000001
186 001256 000300      XN1      SWAB R0
          000300
187 001262 033727      XN3      BIT @#XCSR, #XNRDY
188 001262 175614
          000200
191 001272 001774      BEQ XN1
192 001274 110037      MOVB R0, @#XBUF
          175616
193 001300 000304      DFC R4
194 001302 001767      RTS XN1
195 001304 000205
199 001306 057032      DEFTY:      RAD50 /00J0R100J0R1/
          001310
          057032
          001312
          001314
          001316
198 001316 057032
          001310
          057032
          001312
200 001332 057032
          001310
          057032
201 001336 057032
          001310
          057032
202 001336 057032
          001310
          057032
203 001336 057032
          001310
          057032
204 001336 057032
          001310
          057032
205 001340 057032
          001310
          057032
206 001340 057032
          001310
          057032
207 001342 057032
          001310
          057032
208 001342 057032
          001310
          057032
209 001366 057032
          001310
          057032
210 001366 057032
          001310
          057032
211 001366 057032
          001310
          057032
212 001366 057032
          001310
          057032
213 001402 057032
          001310
          057032
214 001402 057032
          001310
          057032
215 001437 057032
          001310
          057032
216 001474 057032
          001310
          057032
217 001540 057032
          001310
          057032
218 001567 057032
          001310
          057032
219 001637 057032
          001310
          057032
220 001674 057032
          001310
          057032
221 001744 057032
          001310
          057032
222 001763 057032
          001310
          057032
223 002033 057032
          001310
          057032
224 002047 057032
          001310
          057032
225 002120 057032
          001310
          057032
226 002152 057032
          001310
          057032
227 002224 057032
          001310
          057032
228 002260 057032
          001310
          057032
229 002327 057032
          001310
          057032
230 002374 057032
          001310
          057032
231 002402 057032
          001310
          057032

```

```

232 002476 162      .ASCII /reenter remote control, /<15><12> /contin-
233 002487 040      .ASCII / with another portion, if desired./
234 002601 015      .ASCII <15><12><12>
235 002604 110      MSG1:      .ASCII /R/<15>
236 002604 015      MSG2:      .ASCII <15><12><12><7><7>
237 002606 040      MSG3:      .ASCII / Communication error/
238 002606 070      MSG4:      .ASCII /R3A/<15>
239 002613 072      MSG5:      .ASCII / /00/<15><12>
240 002640 125      MSG6:      .ASCII /U/<15>
241 002640 060      MSG7:      .ASCII /00W/<15>
242 002644 015      MSG8:      .ASCII <15><12>/Incorrectly specified/
243 002644 050000      HEXBFL=20480,      ;WORDS
244 002651      EVEN
245 002651      HEXBUF:      .ASCII <15><12> /40K BUFFER
246 002653      .ASCII <15><12> /40K BUFFER
247 002657      .ASCII <15><12> /40K BUFFER
248 002706      .END PROM

```

# THE UNIVERSITY OF ASTON IN BIRMINGHAM

Mr Ken Demers  
MS-48

United Technologies Research Center  
Silver Lane  
East Hartford  
Conn. 06108  
U S A

The Sumpner Building, 19 Colehill Street, Birmingham B4 7PB  
Tel: 021.359 3611 Ex 559/284

The Department of Electrical and Electronic  
Engineering

ELE/MHA/KA  
22 November 1979

Dear Mr Demers

The enclosed note on the pseudo-random number generator routines  
RAN and RANDU provided in the V.OIC Fortran system might be of  
interest to RT-11 SIG members.

At present I am coding a routine based on the algorithm

$$y_{n+1} = 16807 y_n \pmod{2^{31}-1}$$

which is considered better.

Yours sincerely

22.

Martin Ackroyd

# Notes on the PDP-11 Fortran-Callable Pseudo-Random

## Number Generator Routines RAN and RANDU

Martin H. Ackroyd

November 1979

The PDP-11 Fortran system provides two Fortran callable library subroutines RAN and RANDU for generating pseudo-random floating point numbers in the range 0 - 1. Apart from the information given in the DEC Fortran user manual (reproduced in appendix I), there is no explicit information available about the routines. RAN is used by teletraffic simulation programs used in the Telecommunications Group, so it seemed worthwhile to find out more about it. By running the machine code of each routine through an 'inverse assembler' program to convert it to assembly language, some points can be noted from examination of its code. Appendix II is the assembly language version of RAN, with comments added by the writer.

1. RANDU is identical to RAN except for the means of returning the floating point result. Calls to RAN and RANDU may be intermingled, the same pair of variables being used as the integer arguments.
2. The multiplicative congruential method is used to transform a pseudo random 32-bit double word into a new pseudo-random double word. The algorithm used is

$$Y_{n+1} = a Y_n \pmod{m}$$

where  $Y_n$  = 32-bit number held in I1, I2, the integer Fortran arguments.

$$a = (1+2 \times 2^{16}) = 65539$$

$$m = 2^{31} = 2,147,483,648.$$

The period of the sequence should be  $2^{31}/4 = 536,870,912$ . (R.P. Chambers, "Random number generation on digital computers", IEEE spectrum, vol. 4, pp. 48-56, 1967).

3. Each routine consists of two quite separate parts: one to implement the multiplicative congruential algorithm and one to convert the double word integer in I1, I2 into a floating number.
4. D.G. Childers and A. Durling, *Digital Filtering and Signal Processing*, West, 1975, give a routine from the IBM Scientific Subroutine Package:

SUBROUTINE RANDU (IX, IY, YFL)

IY = IX \* 65539 (multiply by  $2^{16}+3$  and reduce modulo  $2^{32}$ )

IF(IY) 2,3,3

23.

2 IY = IY+2147483647+1 (reduce modulo  $2^{31}$ )  
3 YFL = IY (convert to floating number in 0 to  $2^{31}$ )  
  
YFL = YFL\*.4656613E-9 (normalise to range 0 - 1)  
  
RETURN  
END

(Although ostensibly written in Fortran this routine is machine and compiler dependent.) The algorithm is evidently the same as the DEC RANDU, so any information about tests that have been done on the IBM routine may apply to the DEC one.

5. If pseudo random integers are required, I1 could be used. I1 lies in the range 0 to 32767. The use of I2 is not recommended; the period of its low-order bits may be too short (see Chambers, IEEE Spectrum). An integer-only routine could be derived by lopping off the part of RAN which effects the conversion to floating point.

Appendix I - see RT-11 Fortran user manual

Appendix II:

```
.ENABL LC
; Fortran-callable subroutine RAN from the DEC RT-11 Fortran
; library. This assembly language listing produced
; by processing the linked machine code via a disassembler.
; Comments added by M H Ackroyd, Aston University, November 1979.
;
; The Fortran call is:
;       X = RAN(I1,I2)
; where I1,I2 are the state variables. Initially, I1 and I2 should
; be set to zero or saved values from a previous
; run.
R0=X0
R1=X1
R2=X2
R3=X3
R4=X4
R5=X5
SP=X7
PC=X7
.GLOBAL RAN
;
```

24.

```

*****
; Convert the double integer I1,I2 into a new pseudo random integer
; by multiplicative congruential method.
*****
;
RAN:  CMPB (R5),#2      ;Correct number of arguments (2) in the
                        ; Fortran CALL?
      BNE ERROR        ;No - use error handling trap
      IST (R5)+        ;no. of args +ve? (?should always be satisfied?)
      MOV (R5)+,R2     ;Address of I1 into R2
      MOV (R5)+,R3     ;Address of I2 into R3
      MOV (R2),R0      ;I1 into R0
      MOV (R3),R1      ;I2 into R1
      BEQ INIT         ;Initialise if I2=0
;
; Multiply double word in R0,R1 by 2
;
      ASL R1
      ROL R0
;
; add double word in I1,I2 to double word in R0,R1
;
      ADD (R2),R0
      ADD (R3),R1
      ADC R0
;
; Add I2 into R0 equivalent to multiplying double word
; in I1,I2 by 2**16 and reducing the result modulo 2**32
;
      ADD (R3),R0
; (multiplication by 1+2+2**16, with modulo 2**32 reduction
; has now been effected)
;
; If the result is positive, do nothing, else change the sign bit
; (reduce modulo 2**31)
;
      BPL NEXT
      ADD #100000,R0
      BPL NEXT        ;?Seems a mite superfluous?
;
; Place new double word in R0,R1 in I1,I2
;
NEXT:  MOV R0,(R2)
      MOV R1,(R3)
;
*****
; Convert the pseudo-random bit pattern in R0,R1 into a uniformly
; distributed floating-point number.
*****
;
      MOV #201,R2      ;Initial value for exponent into R2
;
; Repeatedly multiply the double word in R0,R1 by 2 until overflow occurs
;
AGAIN: ASL R1
      ROL R0
      BCS DONE        ;overflow occurred?
      DEC R2          ;no - reduce exponent and repeat
      BR AGAIN

```

```

;
; Use the bits in the place where the exponent
; goes in the floating number as pseudo random bits in the
; mantissa.
;
DONE:  CLRB R1         ;clear the lower byte of R1 which will have had
                        ; some of its bits cleared in the normalisation
                        ; process above
      BISH R0,R1       ;put the bits from R1, where the exponent will go
                        ; into the cleared space in R0
      SWAB R1          ;swap bytes so that any bits that have been cleared
                        ; (ie when R0,R1 was shifted more than 8 places)
                        ; are at least significant end of word
      CLRB R0          ;clear space for exponent
      BISH R2,R0       ;clear space for exponent
      SWAB R0          ;put exponent in top half of word
;
; rotate the double register R0,R1 to set the exponent in the right
; position with the most significant bit of R0 being the (+) sign bit
;
      ROR R0
      ROR R1
      BR RETURN        ;floating answer is now in R0,R1
                        ;finished!
;
INIT:  MOV #3,R1       ;initialise at start of pseudo random sequence
      INC R0           ;R0,R1 now hold 2**16+3
      BR NEXT
;
ERROR: TRAP 000320
;
RETURN: RTS PC         ;return to calling program
      .END

```

Dear Ken,

I'm writing in response to the article "Simple Bootstrap Loaders for RL01, RK05" in the September 1979 issue of the Minitasker, submitted by T. Miles of the University of Victoria. The procedure for the RL01 bootstrap includes patching block 1 of the volume. Customers should be careful about using this patch, since words 0 through 63 of block 1 contain the bad block replacement table for the RL01 (see the RT-11 System User's Guide, Section 8.2.11.2, "Replacing Bad Blocks").

Version 3 and 3B users can safely modify the replacement table if they use .BAD files rather than replacement blocks to handle bad blocks. They must avoid using DUP with the /R option, and the monitor INITIALIZE command with the /REPLACE option.

For Version 4 of RT-11, the following locations in block 1 are used:

000-201  
204-251  
700-703  
722-773  
776-777

The remaining locations are reserved for DIGITAL; the INITIALIZE code may use them regardless of whether .BAD files or replacement blocks are selected to handle bad blocks.

Sincerely,

*Joyce D. Chernoff*

Joyce Desmarais Chernoff  
RT-11 Software Documentation

## 7 TRACK MAGTAPE DOCUMENTATION

The RT-11 V3B documentation contains incomplete and incorrect information regarding the use of 7 track magtape to transfer information to or from 6 bit character oriented systems. The documentation will be corrected in the next release of RT-11.

## TERMINAL PORTS ON THE PDT-11

All of the PDT terminal ports require that the EIA data terminal ready signal (connector pin 20) be asserted by the external equipment in order for the ports to function normally. If this pin is left unconnected or is not asserted, the transmitter CSR ready bit corresponding to that port will always remain cleared, and programs will typically hang waiting for the bit to become set (or for the corresponding interrupt), in order to do their output. (The receiver portion is unaffected by this signal; characters received by the port are made available to the software regardless of the condition of this signal.)

In addition, the ports do not produce a data set ready signal (on pin 6). Therefore, if the external equipment or cable does not supply a DTR signal, the usual technique of tying pins 6 and 20 together on the PDT-side EIA connector will be ineffectual in supplying an asserted DTR signal; the signal must somehow be asserted externally.

Although these conditions should not be a problem with DEC terminals or cabling, they may present problems when dealing with non-DEC terminals or non-standard cabling.

## VIRTUAL ARRAYS ON THE 11/23 (FORTRAN IV/RT-11 V2.1)

Use of virtual arrays under the SJ and FB monitors on 11/23 based systems requires that the SJ and FB monitors be patched to run in 28K words; this is a customization patch on page 2-36 of the RT-11 System Generation Manual entitled "Running RT-11 in Less Memory Than Is Available".

Also note that FORTRAN IV/RT-11 V2.1 programs cannot be run as virtual jobs.

## MAXIMUM THROUGHPUT ON THE LA120

The LA120 will usually run at its maximum 180 character per second speed whenever it is operated at baud rates of 2400 or greater. There are cases, however, that require the maximum possible baud rate to achieve maximum throughput, e.g., when large numbers of blanks are being printed. For this reason, it is recommended that the LA120 be operated at 9600 baud whenever possible. (When host XON/XOFF support is not available, 1200 baud will usually be the maximum "safe" speed.)

-----  
PAST SYMPOSIUM INFORMATION  
-----

TAPE COPY

We produced 81 tapes at the Fall 79 DECUS symposium in San Diego. They included 350 files the first of which is an annotated directory (TAPE.DIR). I wish to offer my thanks to both those of you who contributed material for the tape and to those of you who worked to produce the copies.

I am willing to continue making additional copies on request. The rules of this game are simple. A tape in reusable packing along with return postage (not cash or check) is required. Any tape arriving without the reusable packing and return postage will be considered as a very welcome gift. We now have four tapes, the first three will each fit on a 600 ft reel, the last on a 1200 ft reel and all four will fit on a 2400 ft reel. The tapes are 1978 Chicago, 1978 San Francisco, 1979 New Orleans and 1979 San Diego.

Starting with the Spring 80 DECUS symposium we will join with several of the other SIGs and let the DECUS library handle all reproduction and distribution of our tape. We will continue to accept submissions (on floppy disk and mag tape) and build our master tape at the symposium. LUGs will receive a free copy of the tape to distribute as they see fit. Others may order copies from the DECUS library for a small fee (\$20 for Fall 79 tapes). After some period of time copies of the tape will be available via the regular DECUS library distribution for a higher fee.

Tape distribution through the DECUS library adds some formal requirements. All contributors MUST sign a release for the material they submit. No proprietary or licensed software in whole or part is allowed. Also, a short written description of the material is required (machine readable form will be a great aid to your master tape builder). An excellent example of a written description appears as the first entry in the annotated directory of the Fall 79 DECUS RT SIG tape (the file DECUS.TXT on the tape).

For the next symposium in Chicago I need some help for building the master tape. I am looking for a local shop, preferably near the symposium hotel (Hyatt Regency O'Hare), that would be willing to permit me to use their equipment for building our RT SIG master tape. I'll need at minimum a system having RX01s or RX02s and 9-track mag tape (2400 ft reels). A larger disk such as RK05, RL01 or RL02 would be very desirable. If necessary I can bring my own bootable RK05 disk cartridge. Actual use of the system would be during the evening hours. If anyone is willing to volunteer their shop and services please contact me.

Nick Bourseois / 1736  
Sandia Laboratories  
P. O. Box 5800  
Albuquerque, NM 87185  
(505) 844-8088

ANNOTATED DIRECTORY OF THE 1979 FALL RT-11 DECUS TAPE  
SAN DIEGO, CA.

\*\*\*\*\*

This file describes the contents of the RT-11 development group's submissions to the DECUS swap tape for Fall 1979 DECUS in San Diego.

<2042 BLKS TOTAL>

DECUS.TXT            This file

TECO kit:

TECO.SAV            The runnable TECO program  
CRTASM.COM          Assembly instructions for scope module  
TECLNK.COM          Link instructions for TECO  
CRTPRE.MAC          Supplied DEC-standard scope module prefix  
                    and code  
CRTRUB.OBJ          Object modules to recreate TECO

SCREEN.OBJ  
SCRINS.OBJ  
SCROLL.OBJ  
TECO.OBJ  
TECOIO.OBJ  
TIOBCD.OBJ  
TIOEIO.OBJ  
TIOENC.OBJ  
TIOFET.OBJ  
TIOIAS.OBJ  
TIOINI.OBJ  
TIOIFS.OBJ

TECO macros, compressed

LOCAL.TEC  
SEARCH.TEC  
SQU.TEC  
TYPE.TEC  
VTEDIT.TEC  
TECO.INI

TECO macros, uncompressed

LOCAL.TES  
SEARCH.TES  
SQU.TES  
TECO.TES  
TECOIN.TES  
TYPE.TES  
VTEDIT.TES  
TECO.DOC  
TECORN.DOC  
VTEDIT.DOC

The latest TECO manual  
Release notes for TECO V35  
VTEDIT manual

# Othello game playing program:

OTHELLO.COM Build instructions for OTHELLO  
 OTHELLO.FOR Sources  
 VTHELO.FOR  
 OLOGIC.FOR  
 OTHELLO.SAV Runnable OTHELLO game  
 VTHELO.SAV Virtual version (for XM under RT-11 V4)

# Master-mind game playing program:

MMIND.COM Build instructions  
 MMIND.FOR Sources  
 VMIND.FOR  
 MLOGIC.FOR  
 MTEXT.MAC  
 EXTEND.OBJ A binary module  
 MMIND.SAV Runnable Master-mind game  
 VMIND.SAV Virtual version (for XM under RT-11 V4)  
 MMIND.REL Foreground version

# Plane landing game program:

OPLANE.SAV Ordinary runnable version  
 FPLANE.REL Foreground version  
 VPLANE.SAV Virtual version (for XM under RT-11 V4)

# Other useful items:

BANNER.SAV Prints gothic or Roman letter banners  
 LIFE.SAV Life program  
 LIFE.TXT and explanation  
 MTYSET.MAC MTYSET example from RT Support manual;  
 MTYSET.SAV auto-baud and terminal type determination  
 SIGNON.MAC Useful start-up program to require date/time  
 SIGNON.SAV  
 RTMON.MAC Foreground system status display (V4 only)  
 TTYSET.SAV Set terminal characteristics (contains HELP)  
 VDT.OBJ Virtual/multi-terminal version of ODT  
 VM.MAC VM handler for memory over 28K (V4 only)  
 SY.MAC System parameter handler (V4 only)

\*\*\*\*\*

JOHN R. BARR, PHD PASCAL  
 ASSISTANT PROFESSOR DOCUMENTATION TO BE IN  
 COMPUTER SCIENCE DEPARTMENT SYMPOSIUM PROCEEDINGS  
 UNIVERSITY OF MONTANA  
 MISSOULA  
 (406) 243-2883

10-Dec-79  
 RUNTIME.PAC 112 13-Nov-79 NPASL .OBJ 24 13-Nov-79  
 CPASL .OBJ 7 13-Nov-79 SELECT.OBJ 6 13-Nov-79  
 SELECT.SAV 12 13-Nov-79 UTIL .PAC 18 13-Nov-79  
 MISC .PAC 51 NBUGS .RNO 27 22-Oct-79  
 NEWS .RNO 37 22-Oct-79 PASGUI.RNO 84 22-Oct-79  
 10 Files, 378 Blocks 30.

10-Dec-79  
 PASS1 .PAS 196 02-Dec-79 PASS1 .OBJ 122 02-Dec-79  
 PASS1 .SAV 74 02-Dec-79 NPASL .OBJ 24 02-Dec-79  
 CPASL .OBJ 7 13-Nov-79  
 5 Files, 423 Blocks  
 10-Dec-79  
 NPASL .OBJ 24 02-Dec-79 CPASL .OBJ 7 13-Nov-79  
 PASS2 .PAS 204 06-Dec-79 PASS2 .OBJ 119 06-Dec-79  
 PASS2 .SAV 86 06-Dec-79  
 5 Files, 440 Blocks

\*\*\*\*\*

NICK BOURGEOIS / 1736 BASIC CROSS REFERENCE PROGRAMS  
 SANDIA LABORATORIES SELECTED RT-11 V03B PATCHES  
 PO BOX 5800 FORTRAN V2.1 PATCHES  
 ALBUQUERQUE, NM 87185 BASIC V2 PATCHES  
 (505) 844-8088

10-Dec-79  
 CROSS .BAS 22 10-Dec-79 CROSS0.BAS 7 10-Dec-79  
 CROSS1.BAS 13 10-Dec-79 CROSS2.BAS 8 10-Dec-79  
 CROSSV.BAS 23 10-Dec-79 CROSS .RNO 6 10-Dec-79  
 CROSS .DOC 7 10-Dec-79 MON016.BAT 3 10-Dec-79  
 MON015.BAT 7 10-Dec-79 MON017.BAT 7 10-Dec-79  
 MON018.BAT 5 10-Dec-79 MON019.BAT 2 10-Dec-79  
 MON020.BAT 9 10-Dec-79 FXV02 .MAC 1 10-Dec-79  
 PAT10 .COM 2 10-Dec-79 SRC006.BAT 4 10-Dec-79  
 UTL013.BAT 4 10-Dec-79 MON021.BAT 3 10-Dec-79  
 PAT11 .COM 2 10-Dec-79 PAT12 .COM 2 10-Dec-79  
 PAT06 .MAC 1 10-Dec-79 PAT06 .FOR 1 10-Dec-79  
 FXV11 .MAC 1 10-Dec-79 PAT11 .MAC 1 10-Dec-79  
 PAT07 .FOR 1 10-Dec-79 PAT09A.MAC 1 10-Dec-79  
 PAT11 .FOR 1 10-Dec-79 PAT12 .MAC 1 10-Dec-79  
 PAT12A.FOR 1 10-Dec-79 PAT12B.FOR 1 10-Dec-79  
 PAT09B.MAC 1 10-Dec-79 PAT13 .MAC 2 10-Dec-79  
 PAT01 .MAC 1 10-Dec-79 UTL001.BAT 3 10-Dec-79  
 UTL002.BAT 2 10-Dec-79 MON006.BAT 4 10-Dec-79  
 SRC001.BAT 2 10-Dec-79 SRC002.BAT 2 10-Dec-79  
 BAS001.COM 2 10-Dec-79 BAS001.TEC 1 10-Dec-79  
 MSC002.MAC 1 10-Dec-79 UTL010.MAC 1 10-Dec-79  
 MSC002.COM 2 10-Dec-79 UTL010.COM 1 10-Dec-79  
 MON013.BAT 3 10-Dec-79 MON014.BAT 5 10-Dec-79  
 UTL007.BAT 2 10-Dec-79 UTL011.BAT 2 10-Dec-79  
 UTL008.BAT 3 10-Dec-79 UTL012.BAT 3 10-Dec-79  
 MON001.BAT 2 10-Dec-79 PATCH .COM 2 10-Dec-79  
 MON005.BAT 4 10-Dec-79 MON007.BAT 2 10-Dec-79  
 MON004.BAT 6 10-Dec-79 MON003.BAT 5 10-Dec-79  
 MSC001.BAT 2 10-Dec-79 SRC003.BAT 4 10-Dec-79  
 MON010.BAT 3 10-Dec-79 UTL003.BAT 2 10-Dec-79  
 UTL004.BAT 2 10-Dec-79 UTL005.BAT 2 10-Dec-79  
 UTL006.BAT 2 10-Dec-79 MON011.BAT 8 10-Dec-79  
 MON012.BAT 2 10-Dec-79 MON009.BAT 3 10-Dec-79  
 MON002.BAT 7 10-Dec-79 PAT13 .FOR 1 10-Dec-79  
 FXV08 .MAC 1 10-Dec-79 PAT09 .FOR 2 10-Dec-79





# SOURCES FOR THE RT-11 DEVELOPMENT GROUPS SUBMISSION

```

10-Dec-79
RTMON .MAC      32 06-Nov-79    MTYSET.MAC      17 01-Nov-79
SIGNON.MAC      4 21-Nov-79    OTHELO.FOR     37 20-Nov-79
SPEED .FOR      1 12-Nov-79    POWER .FOR      1 12-Nov-79
ALT .FOR        1 12-Nov-79    PANEL .FOR      4 12-Nov-79
ATT .FOR        1 12-Nov-79    VERTCL.FOR     1 12-Nov-79
VERT .FOR       1 12-Nov-79    COURSE.FOR     1 12-Nov-79
BAR .FOR        1 12-Nov-79    CLIMB .FOR     1 12-Nov-79
CURMOV.FOR      2 12-Nov-79    LEFT .FOR      1 13-Nov-79
RIGHT .FOR      1 13-Nov-79    LEVEL .FOR     1 13-Nov-79
PAGE1 .FOR      2 20-Nov-79    PAGE2 .FOR     3 20-Nov-79
MTEST .FOR      9 26-Nov-79    PLANE .FOR     11 03-Dec-79
ERASE .FOR      1 21-Nov-79    VPLANE.COM     1 04-Dec-79
LOGIC .FOR     11 03-Dec-79    OPLANE.COM     1 23-Nov-79
FPLANE.COM      1 23-Nov-79    VPLANE.FOR     1 03-Dec-79
CPLANE.COM      1 04-Dec-79    MTEXT .MAC     2 03-Dec-79
MMIND .FOR     10 04-Dec-79    VMIND .FOR     1 04-Dec-79
MLOGIC.FOR     10 04-Dec-79    VTHELO.FOR     1 04-Dec-79
OLOGIC.FOR     36 04-Dec-79    OTHELO.COM     1 04-Dec-79
MMIND .COM      1 04-Dec-79
37 Files, 212 Blocks

```

\*\*\*\*\*

CARL D. LOWENSTEIN  
MARINE PHYSICAL LAB  
BLDG. 106, NOSC  
SAN DIEGO, CA 92152

(714) 294-3678

RATIONALIZED FORTRAN PROCESSOR, TAKEN FROM  
KERNIGHAN & PLAUGER, SOFTWARE TOOLS DISTRIBUTION TAPE.  
INTERFACED TO RT-11 FILE SYSTEM THROUGH COMMAND STRING INTERPRETER.

RATFOR.SAV 61 21-Nov-79 RATFOR.FOR 367 21-Nov-79

## USER APPLICATION WORKSHOP

Once again the workshop session was a great success. There were four informal presentations followed by lots of interactive exchange from the floor. The informal presentations were:

Greg Adams -- An Air Traffic Control Simulator.

Randy Kaplan -- Using MiniMNCs for Data Collection.

Ned Rhodes -- A Data Acquisition System.

Mark Servello -- Monitoring Pulmonary-Lung Functions.

Nick Bourgeois

## UPCOMING SYMPOSIUM INFORMATION

There will be an RT-11/PDP-11 assembly language tutorial scheduled during the Spring, 1980 Symposium. Art Hermes will discuss addressing modes and Ray Streckbein will discuss program requests. This session is intended for those not experienced in PDP-11 assembly language programming. Those planning to attend should read "Fundamentals of PDP-11 Programming by R. W. Southern. This self-paced book is available at the DEC Training Center Bookstore in Bedford, Mass.

## RT-11 PATCHES

### MDUP FAILURE ON RL01 (RT-11 V3B)

After booting a mastape, MDUP(.MT or .MM) will hang upon any attempt to access an RL01. The following patch will correct this problem.

#### .R PATCH

```

FILE NAME--
*MDUP.MT or MDUP.MM<RETURN>
*160/ 0 46464<LINE FEED>
162/ 0 340<RETURN>
*E

```

-----  
SPR'S  
-----



**State Electricity Commission of Victoria**

Herman Research Laboratory  
Howard Street, Richmond, Vic. 3121  
Telephone 429 1511  
Telex 31153  
Cable & Telegraphic Address: Electroom Melbourne

Our Reference  
RC

Dear Ken

The enclosed copy of an SPR response from DEC has been received in response to my SPR which you published in Volume 5, No 4, of the Mini-Tasker.

Since the fault reported was generated by a whole class of programs similar to that given, I believe that the meaning of the response is:

'The FORTRAN compiler does not support mixed-mode arithmetic when the in-line code option is used.'

As this is a significant restriction on the usefulness of the compiler, I believe it should be widely publicised.

Yours sincerely

Geoffrey R Grinton  
SCIENTIFIC OFFICER

D I G I T A L

SPR RESPONSE

SUBJECT: SPR NUMBER 11-25412

	SYSTEM	SYS VERS	PRODUCT	PROD VERS	COMPONENT
SOFTWARE:	RT-11	38	FORTRAN-IV	2.1	COMPILER

**PROBLEM  
STATEMENT**

Compiler generates incorrect code when inline code option is used.

**RESPONSE**

This will be fixed in the next release of FORTRAN IV, in the meantime please either use threaded code option or use a real dummy variable or apply FLOAT on the integer variable.

COBOL FOR RT-11

RT-11 and TSX users now have the option of COBOL programming with the announcement by S&H Computer Systems, Inc. of COBOL-PLUS, a full ANS '74 Standard COBOL compiler and run-time system which will run on any PDP-11 or LSI-11.

Designed to run programs much larger than physical memory, the COBOL-PLUS compiler allows automatic virtual memory program segmentation by a "least-recently-used" technique to keep the most active set of segments in memory. COBOL-PLUS run-time routines for OPEN-CLOSE, input-output, and ISAM are also managed in this fashion.

COBOL-PLUS also provides interactive screen-handling capabilities through the use of positional ACCEPT and DISPLAY statements. Powerful input-output facilities are provided by support of sequential, relative, and indexed-sequential files. Indexed access files are dynamically self-reorganizing. Record locking facilities are provided to synchronize access to shared relative and indexed organization files. Disk sorts are performed by the highly efficient and field-proven RTSORT facility.

A great deal of programming flexibility is provided to the COBOL-PLUS programmer by support of a variety of data types, including full support of usage type DISPLAY and COMPUTATIONAL data items. DISPLAY type items may have leading, separate, trailing separate, or trailing non-separate signs and may be up to 18 digits long. COMPUTATIONAL data items may be 1, 2, or 4 words long.

COBOL-PLUS implements most of the ANS '74 Level 2 language features. Included are the COMPUTE statement; the OCCURS DEPENDING ON clause; level 66, 77, and 88 data items; complex

conditional expressions; and the PERFORM VARYING and MOVE CORRESPONDING statements.

For more information contact H. Dale Coleman, S&H Computer Systems, Inc., 1027 17th Ave. South, Nashville, Tennessee, (615) 327-3670.

Software Dynamics has a communications package to enable telecommunications with Data Terminal Systems series 500 Electronic Cash Registers. This package uses Binary Synchronous Communications (BSC) for communication between the host and cash register(s). Requires RT-11 F/B monitor, V3R or later and either a DUV11, DU11 or DUF11 interface.

For further information contact:

Software Dynamics, Inc.  
1000 Yale Avenue  
Wallington, CT 06492  
(203) 269-4998



Warner-Eddison Associates, Inc.  
186 Alewife Brook Parkway, Cambridge, Massachusetts 02138 (617) 661-8124

Dear Mr. Demers:

Enclosed is information about INMAGIC<sup>TM</sup> data management software. We think this will be of interest to DECUS Newsletter readers.

Warner-Eddison Associates developed INMAGIC<sup>TM</sup> for its work in organizing libraries and information centers. There has been such a demand for software with applications like those of INMAGIC<sup>TM</sup> that we are now selling the software itself.

If you have any questions about INMAGIC<sup>TM</sup> or about Warner-Eddison's services, please feel free to call.

Sincerely yours,

A handwritten signature in dark ink, appearing to read "Alexandra Baker". The signature is fluid and cursive, with the first name "Alexandra" being more prominent than the last name "Baker".

Alexandra Baker  
Development Division

INMAGIC™ SOFTWARE  
AVAILABLE FOR DEC PDP-11s

INMAGIC™ data management software from Warner-Eddison Associates is now available for use on DEC PDP-11s under the RT-11, RSX or RSTS/E operating systems.

INMAGIC™ allows users to build, update and search their own databases for information records describing technical reports, books, proposals, vendor catalogs, client and personnel files.

Features include: unlimited number and length of fields within each record and unlimited subfields within each field; searching by Boolean, AND, OR, and NOT logic; sorting letter-by-letter, word-by-word, numerically or chronologically. Library cards, mailing lists and labels, or catalogs are possible print formats. Users may also define their own report styles. INMAGIC™ is written in FORTRAN IV.

The INMAGIC™ User's Manual describes INMAGIC™'s five working environments: SELECT, SORT, MAINTAIN, DEFINE, and AUDIT, and provides system documentation.



DIGITAL EQUIPMENT COMPUTER USERS SOCIETY  
ONE IRON WAY, MR2-3/E55  
MARLBORO, MASSACHUSETTS 01752

BULK RATE  
U.S. POSTAGE  
PAID  
PERMIT NO. 129  
NORTHBORO, MA  
01532

**MOVING OR REPLACING A DELEGATE?**

Please notify us immediately to guarantee continuing receipt of DECUS literature. Allow up to six weeks for change to take effect.

- ☐ Change of Address  
☐ Delegate Replacement

DECUS Membership No.: \_\_\_\_\_

Name: \_\_\_\_\_

Company: \_\_\_\_\_

Address: \_\_\_\_\_

\_\_\_\_\_

State/Country: \_\_\_\_\_

Zip/Postal Code: \_\_\_\_\_

Mail to: DECUS - ATT: Membership  
One Iron Way, MR2-3  
Marlboro, Massachusetts 01752 USA

Affix mailing label  
here. If label is not  
available, print old  
address here.  
Include name of  
installation, com-  
pany, university,  
etc.